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03316263 **Image available**
SCANNER DEVICE

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ABSTRACT

PURPOSE: To increase the processing speed by providing a subscanning control means which can control the subscanning speed to plural stages and a speed detecting means which detects the image data taking-in average speed of a host device and setting a maximum subscanning speed in such range based on this image data taking-in average speed that a buffer memory does not overflow.

CONSTITUTION: A transfer speed detecting circuit 30 outputs a detected transfer speed to a CPU 31, and the CPU 31 sets the subscanning speed based on this transfer speed so that image data taken into a buffer memory 27 and image data read out from the buffer memory 27 are balanced, and the rotating speed of a subscanning motor 32 is controlled based on the set subscanning speed. That is, the CPU 31 properly controls the rotating speed of the subscanning motor 32 in plural stages, and the rotating speed corresponding to the set subscanning speed is selected to control the subscanning speed. At the time of setting the subscanning speed, the CPU 31 sets such highest subscanning speed that the buffer memory 27 does not overflow.